**EXPERIMENT 6--HAZARDS AND GLITCHES**

**FINAL REPORT**

**I. *Hazards in the original circuit (Figure 8 .8-a in the textbook).***

a) Sketch the timing diagrams obtained in part 1 of the procedure. Clearly indicate the glitches.



*Low -> High*



*High -> Low*

b) What is the length of each glitch measured in part 1 of the procedure?

**II. *Hazards in the modified circuit with NOT gates.***

a) Sketch the timing diagrams obtained in part 2 of the procedure. Clearly indicate the glitches.



*Low -> High*



*High -> Low*

b) What is the length of each glitch measured in part 2 of the procedure?

**III. *Elimination of hazards (Part 3 of the procedure).***

a) Draw the modified circuit that eliminates the hazard in the space below.

b) Sketch the timing diagrams obtained in part 3 of the procedure.



*Low -> High*



*High -> Low*

c) Explain why the glitches disappeared.